

8

7

6

5

4

3

2

1

D

D

C

C

B

B

A

A

2 SHEET

TCLOCKJ, TDATAJ

TCLOCK, TDATA

CAMAC DATAWAY

POWER, BYPASS

CONNECTOR  
CAMAC

TEV CLK  
DEC

TCLOCKJ,  
TDATAJ

TCLOCK, TDATA

DATA  
D(0:7)

SHEET 3

CAMAC  
SLAVE

CAMAC  
DATAWAY

READ LINES  
R(1:24)

WRITE LINES  
W(1:24)

F/A LINES  
Z, X, Q, S1, S2

CONTROL  
PBRST  
CPURST  
VINT  
VIACK  
CPUWAIT  
10MHZ  
CAMDIS  
DS

LAM, LAMEN,  
CAMACN

AD(0-7)

SHEET 4

DUAL SIO

TO FRONT  
PANEL

CONTROL  
SIOIRQ  
IORD  
IOWR  
SIOSEL  
CPURST

ADDRESS  
ADB(1:4)

DATA  
DB(0:7)

SHEET 5

C1045  
FRONT  
PANEL

(OP0-3)  
TXDATA  
RXDATA  
PBRST  
PWR  
CAMACN  
LAM  
LAMEN  
HRTBT  
TCLK

SHEET 6

C1045ARC  
INTERFACE

ADDRESS  
ADB(0:8)

DATA  
DB(0:15)

R/W  
RDSTB  
LANIRQ  
DATADIR  
CPURES  
INWAIT

SHEET 7

BLKTRN

CONTROL  
20MHZ  
CPURST  
SERRD  
SERWR  
SERDY  
SERDAV

TO FRONT  
PANEL

DB(0-7)

SHEET 8

1045CPU

ADDRESS  
ADB(0:15)

DATA  
DB(0:15)

CONTROL

SHEET 9

REF. C1045.SCH

DRAWN		DATE	C1045 TOP	
CHECKED	R. FLOOD	9/29/89	SIZE C	DWG. NO. 0880-EC-172840
ISSUED			SCALE	REV.
			SHEET	1 OF 9

8

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8

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3

2

1

D

D

C

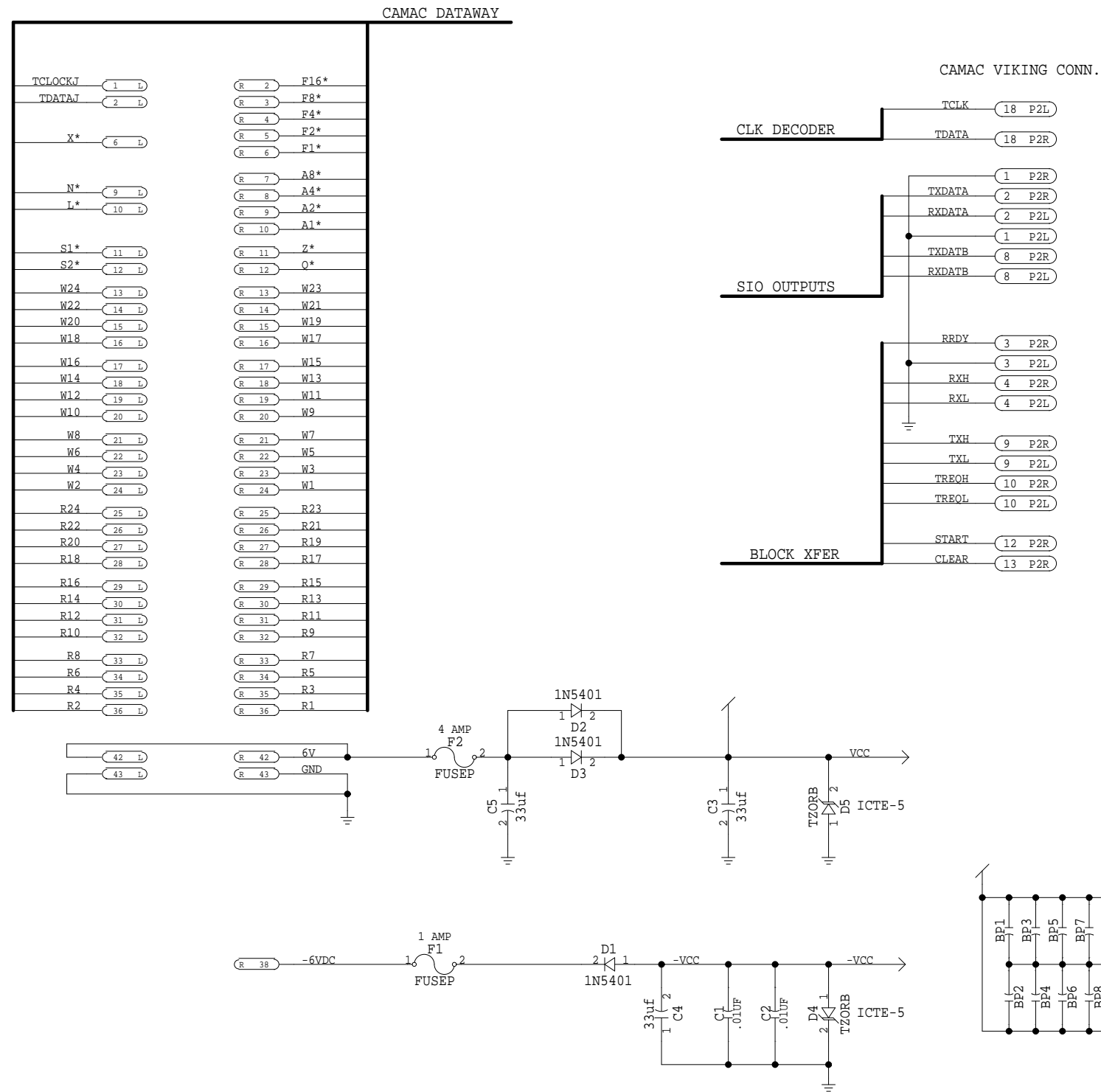
C

B

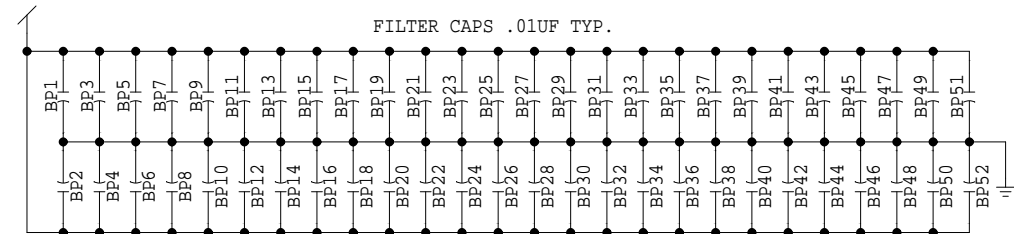
B

A

A

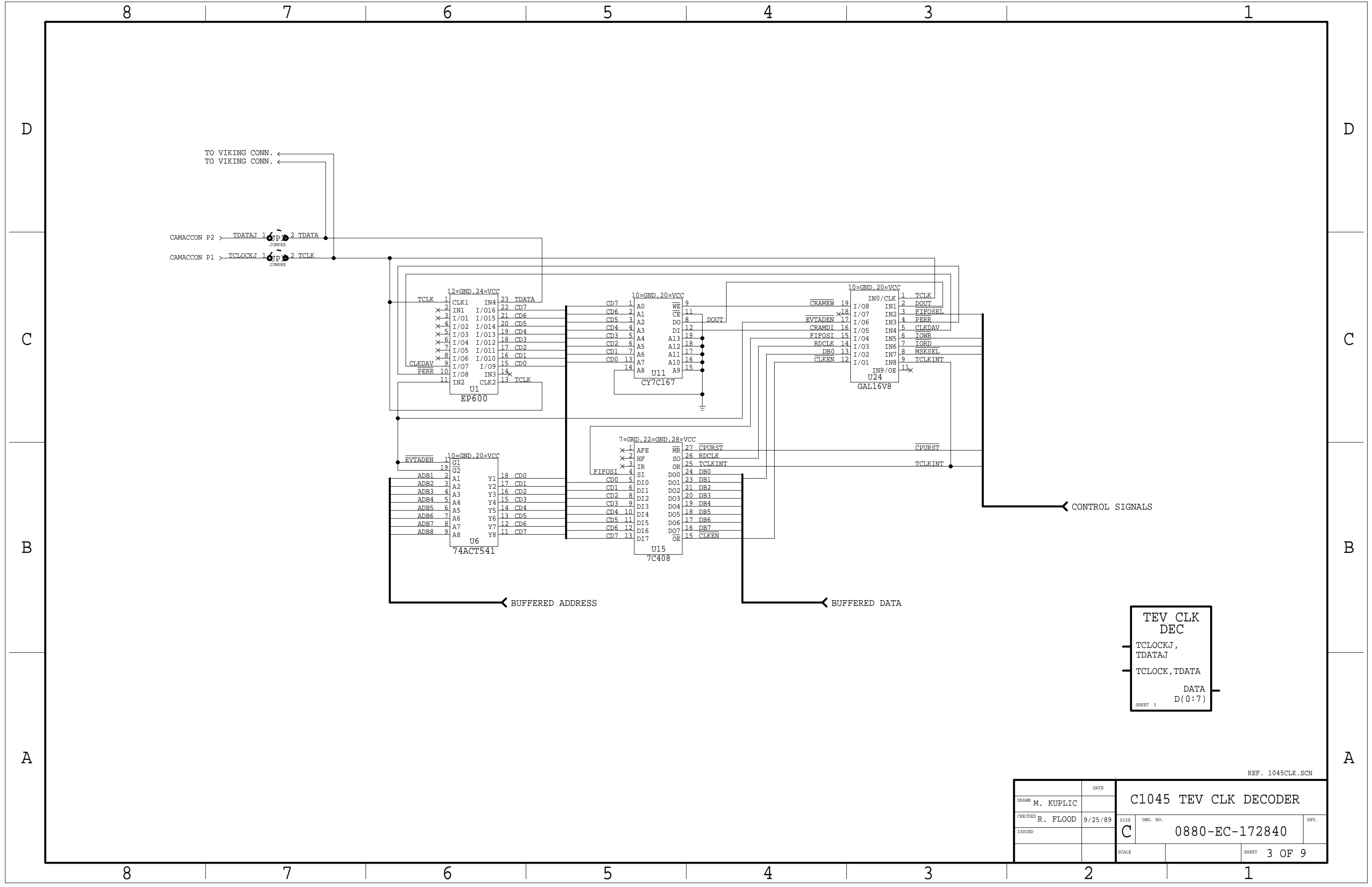


**CAMAC CONNECTOR**  
 POWER, BYPASS  
 CAMAC DATAWAY  
 TCLOCKJ, TDATAJ  
 TCLOCKJ, TDATAJ  
 SHEET 2



REF. 1045CMCN.SCH

DRAWN	R. FLOOD	DATE		1045 CAMAC CONNECTOR	
CHECKED	R. FLOOD	12/20/89	SIZE	DWG. NO.	REV.
ISSUED			C	0880-EC-172840	
SCALE			SHEET 2 OF 9		



TO VIKING CONN. ←  
 TO VIKING CONN. ←

CAMACCON P2 → TDATAJ 1 2 TDATA  
 CAMACCON P1 → TCLOCKJ 1 2 TCLK

12=GND, 24=VCC  
 TCLK 1 CLK1 IN4 23 TDATA  
 × 2 IN1 I/O16 22 CD7  
 × 3 I/O1 I/O15 21 CD6  
 × 4 I/O2 I/O14 20 CD5  
 × 5 I/O3 I/O13 19 CD4  
 × 6 I/O4 I/O12 18 CD3  
 × 7 I/O5 I/O11 17 CD2  
 × 8 I/O6 I/O10 16 CD1  
 CLKDAV 9 I/O7 I/O9 15 CD0  
 PERR 10 I/O8 IN3 14  
 IN2 CLK2 13 TCLK  
 U1  
 EP600

10=GND, 20=VCC  
 CD7 1 A0 WE 9  
 CD6 2 A1 CE 11  
 CD5 3 A2 DO 8  
 CD4 4 A3 DI 12  
 CD3 5 A4 A13 19  
 CD2 6 A5 A12 18  
 CD1 7 A6 A11 17  
 CD0 13 A7 A10 16  
 A8 U11 A9 15  
 CY7C167

10=GND, 20=VCC  
 CRAMEW 19 IN0/CLK 1 TCLK  
 × 18 I/O8 IN1 2 DOUT  
 EVTADEN 17 I/O7 IN2 3 FIFOS1  
 CRAMDI 16 I/O6 IN3 4 PERR  
 FIFOS1 15 I/O5 IN4 5 CLKDAV  
 RDCLK 14 I/O4 IN5 6 IOR  
 DB0 13 I/O3 IN6 7 IORD  
 CLKEN 12 I/O2 IN7 8 MSKSEL  
 I/O1 IN8 9 TCLKINT  
 IN9/OE 11  
 U24  
 GAL16V8

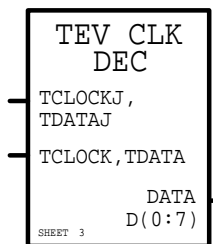
10=GND, 20=VCC  
 EVTADEN 1 G1  
 ADB1 2 A1 Y1 18 CD0  
 ADB2 3 A2 Y2 17 CD1  
 ADB3 4 A3 Y3 16 CD2  
 ADB4 5 A4 Y4 15 CD3  
 ADB5 6 A5 Y5 14 CD4  
 ADB6 7 A6 Y6 13 CD5  
 ADB7 8 A7 Y7 12 CD6  
 ADB8 9 A8 Y8 11 CD7  
 U6  
 74ACT541

7=GND, 22=GND, 28=VCC  
 × 1 AFE MR 27 CPURST  
 × 2 HF SO 26 RDCLK  
 × 3 IR OR 25 TCLKINT  
 FIFOS1 4 SI DO0 24 DB0  
 CD0 5 DI0 DO1 23 DB1  
 CD1 6 DI1 DO2 21 DB2  
 CD2 8 DI2 DO3 20 DB3  
 CD3 9 DI3 DO4 19 DB4  
 CD4 10 DI4 DO5 18 DB5  
 CD5 11 DI5 DO6 17 DB6  
 CD6 12 DI6 DO7 16 DB7  
 CD7 13 DI7 OE 15 CLKEN  
 U15  
 7C408

← BUFFERED ADDRESS

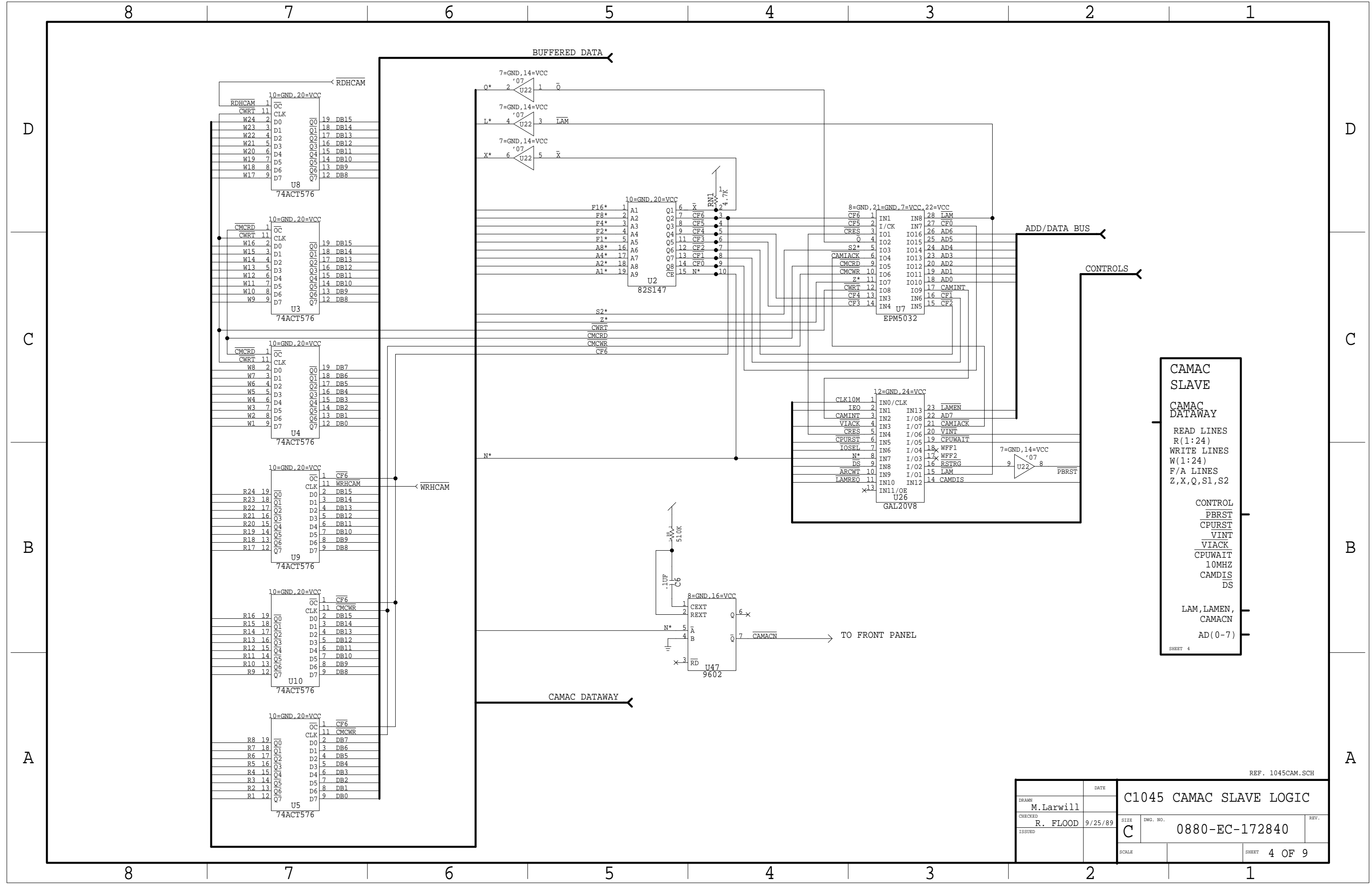
← BUFFERED DATA

← CONTROL SIGNALS



REF. 1045CLK.SCH

DRAWN M. KUPLIC		DATE		C1045 TEV CLK DECODER	
CHECKED R. FLOOD	9/25/89	SIZE	DWG. NO.	REV.	
ISSUED		C	0880-EC-172840		
		SCALE			SHEET 3 OF 9



**CAMAC SLAVE**

**CAMAC DATAWAY**

READ LINES  
R(1:24)

WRITE LINES  
W(1:24)

F/A LINES  
Z, X, Q, S1, S2

**CONTROL**

PBRST  
CPURST  
VINT  
VIACK  
CPUWAIT  
10MHZ  
CAMDIS  
DS

LAM, LAMEN,  
CAMACN

AD(0-7)

SHEET 4

REF. 1045CAM.SCH

DRAWN M.Larwill		DATE 9/25/89		C1045 CAMAC SLAVE LOGIC	
CHECKED R. FLOOD		ISSUED		SIZE C	DWG. NO. 0880-EC-172840
SCALE				SHEET 4 OF 9	

8 7 6 5 4 3 2 1

D

D

C

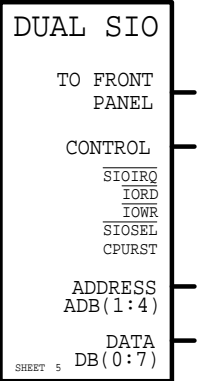
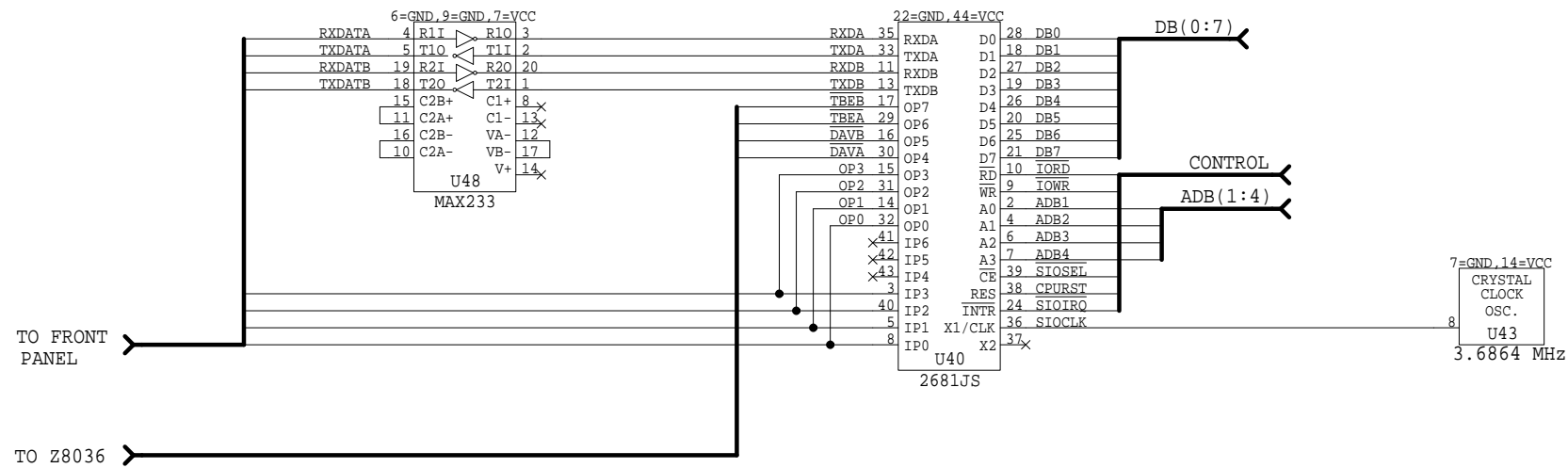
C

B

B

A

A



REF. 1045SIO.SCH

DATE	C1045 SIO LOGIC		
DRAWN R. FLOOD	SIZE C	DWG. NO. 0880-EC-172840	REV.
CHECKED R. FLOOD 9/25/89	SCALE NONE		
ISSUED	SHEET 5 OF 9		

8 7 6 5 4 3 2 1

D

D

C

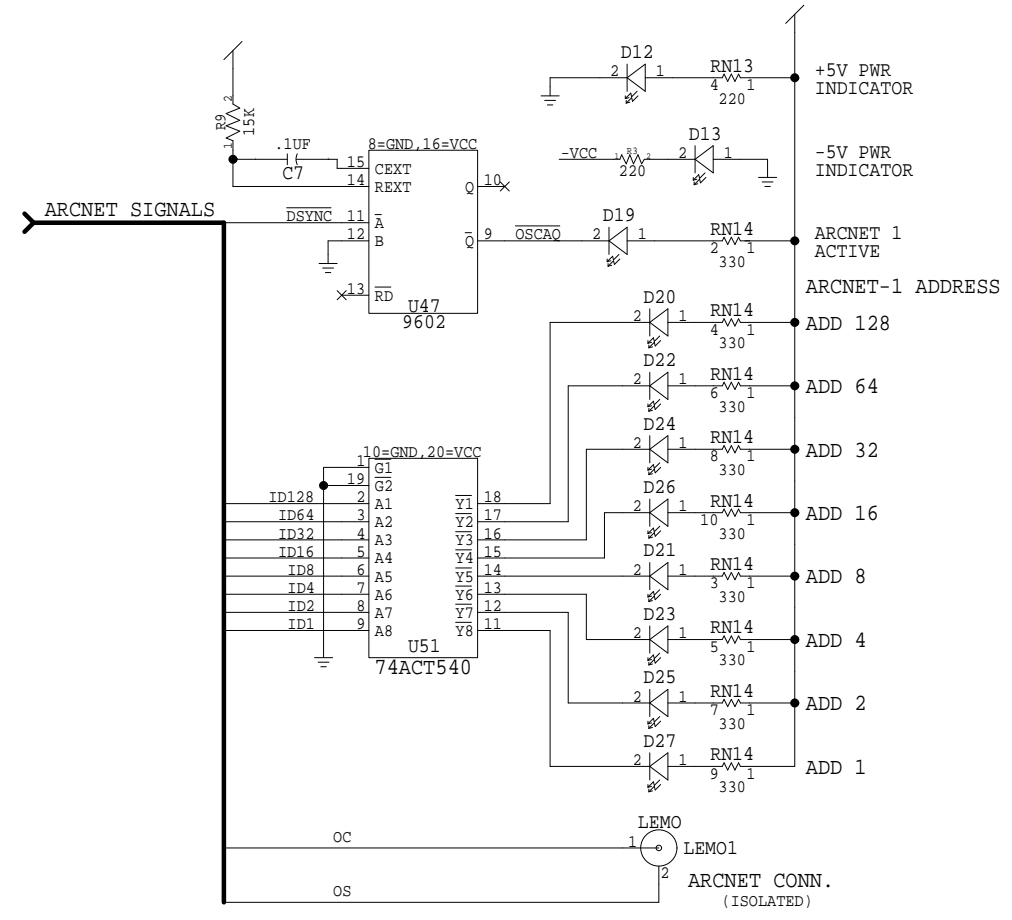
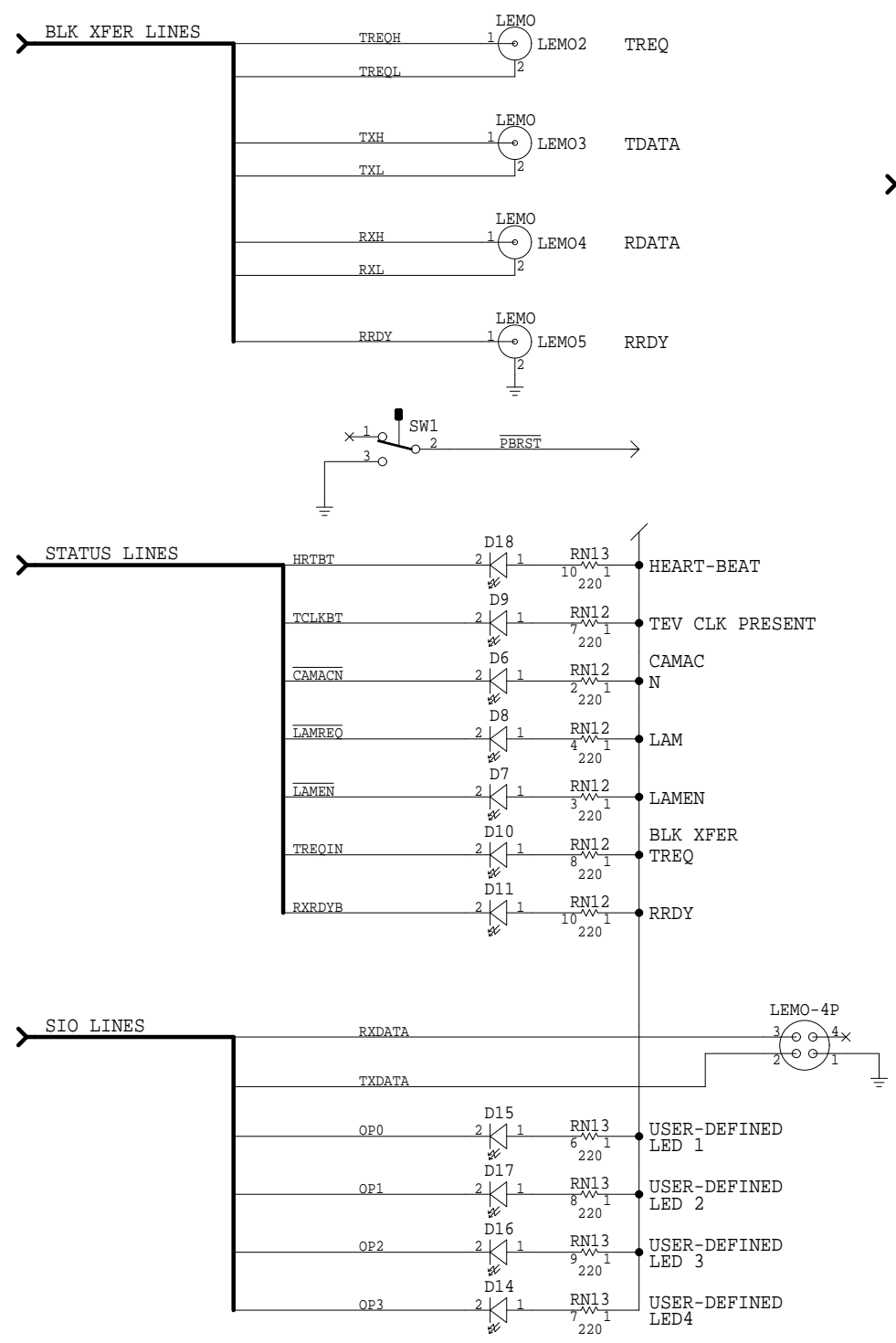
C

B

B

A

A

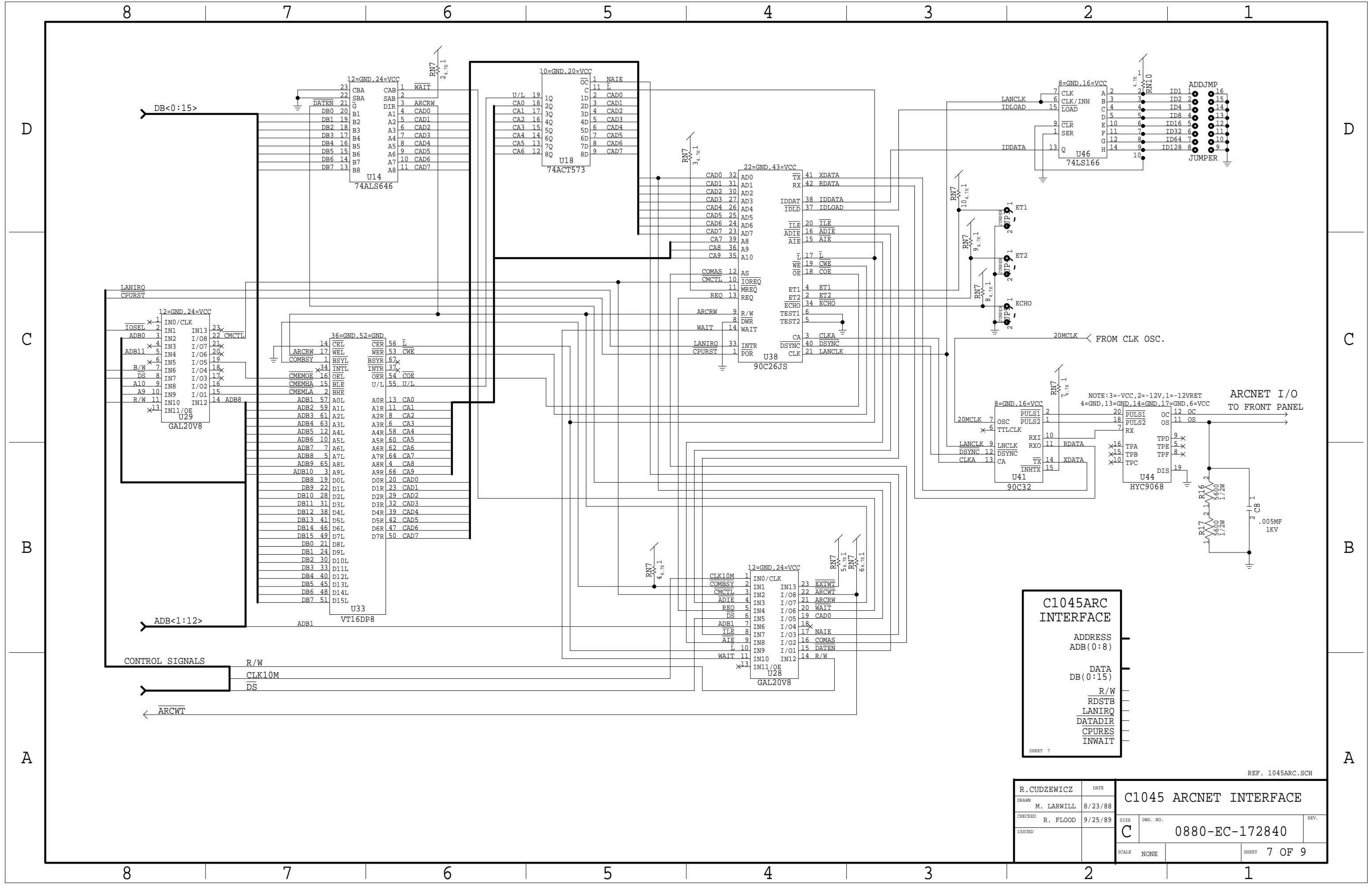


**C1045 FRONT PANEL**  
 (OP0-3)  
 (TXDATA)  
 (RXDATA)  
 PBRST  
 PWR  
 CAMACN  
 LAM  
 LAMEN  
 HRTBT  
 TCLK  
 SHEET 6

REF. 1045FPNL.SCH

DRAWN		DATE		<b>C1045 FRONT PANEL</b>		
R. FLOOD		8/20/88				
CHECKED		DATE		SIZE	DWG. NO.	REV.
R. FLOOD		9/25/89		C	0880-EC-172840	
ISSUED				SCALE	NONE	SHEET 6 OF 9

8 7 6 5 4 3 2 1



**C1045ARC INTERFACE**

ADDRESS  
ADB(0:8)

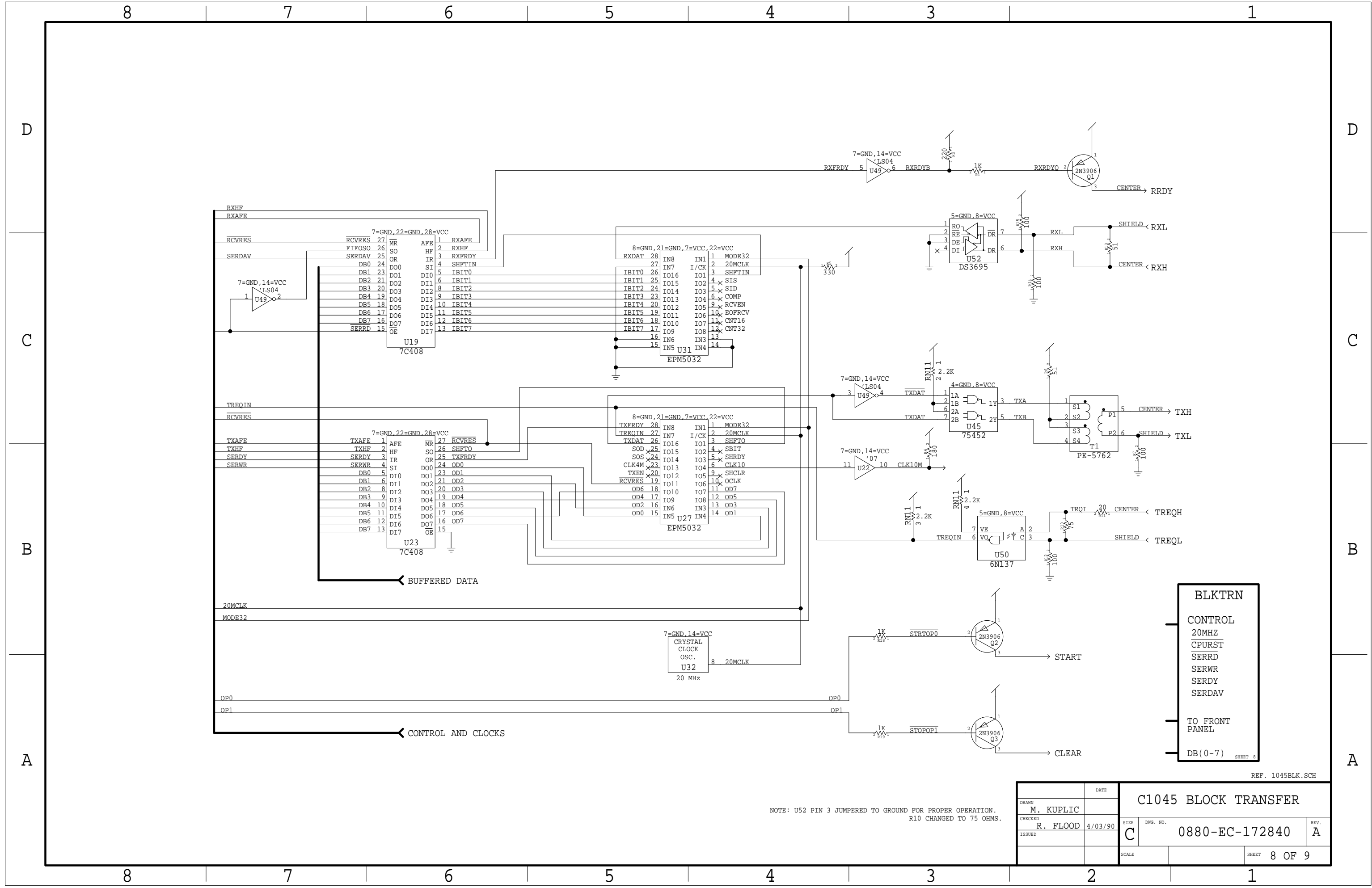
DATA  
DB(0:15)

R/W  
RDSTB  
LANIRO  
DATADIR  
CPURES  
INWAIT

SHEET 7

REF. 1045ARC.SCH

R. CUDZEWICZ		DATE	C1045 ARCNET INTERFACE	
DRAWN	M. LARWILL	8/23/88	SIZE	DWG. NO.
CHECKED	R. FLOOD	9/25/89	C 0880-EC-172840	
ISSUED			SCALE	NONE
			SHEET 7 OF 9	



**BLKTRN**

CONTROL  
 20MHZ  
 CPURST  
 SERRD  
 SERWR  
 SERDY  
 SERDAV

TO FRONT PANEL

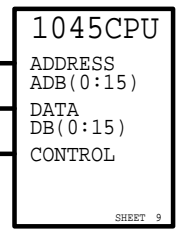
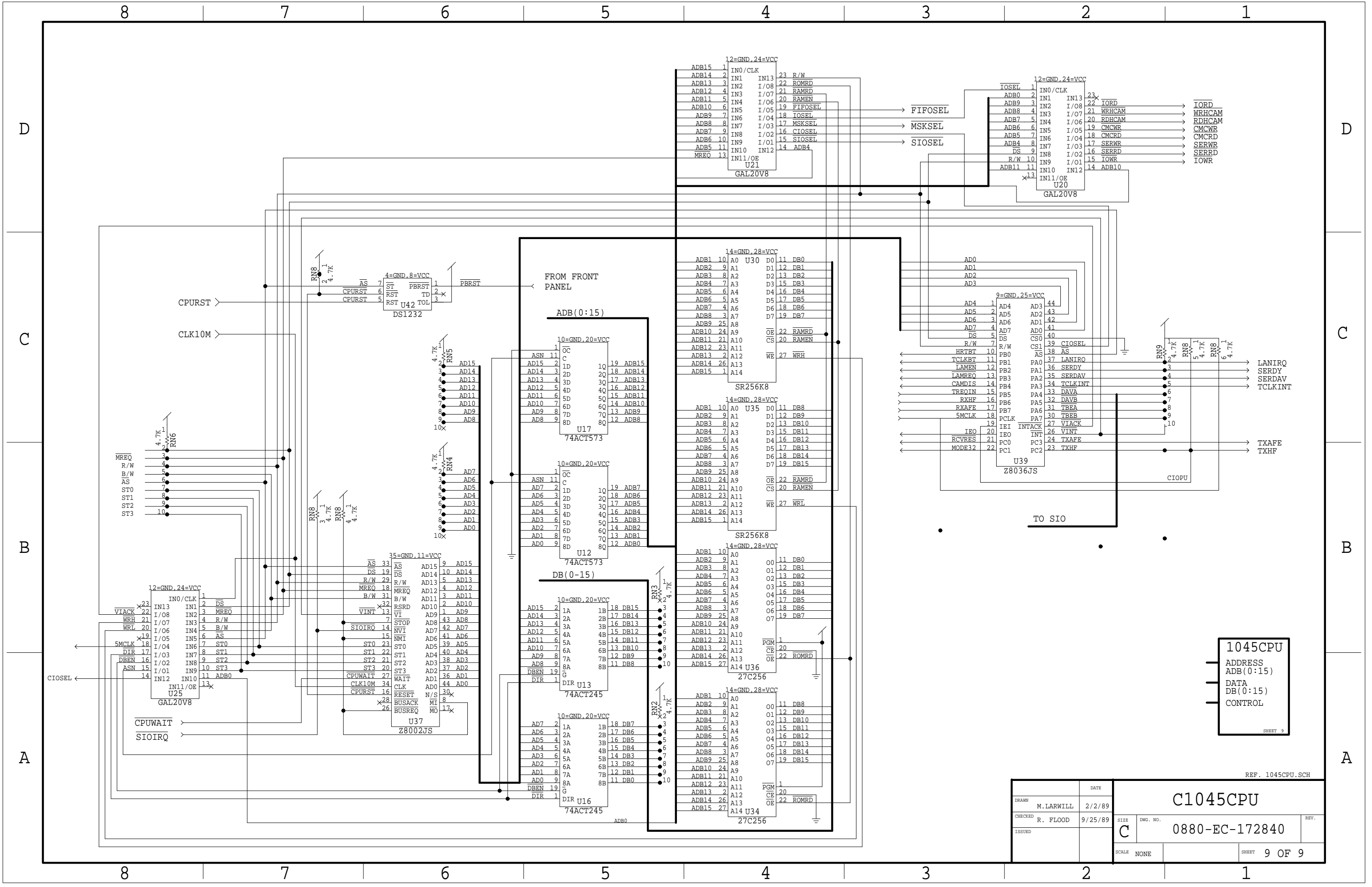
DB(0-7) SHEET 8

NOTE: U52 PIN 3 JUMPERED TO GROUND FOR PROPER OPERATION.  
 R10 CHANGED TO 75 OHMS.

DATE		C1045 BLOCK TRANSFER	
DRAWN M. KUPLIC	CHECKED R. FLOOD	SIZE C	DWG. NO. 0880-EC-172840
ISSUED		SCALE	REV. A
		SHEET 8 OF 9	

REF. 1045BLK.SCH





DRAWN		DATE		REV.	
M. LARWILL		2/2/89		C	
CHECKED		9/25/89		DWG. NO.	
ISSUED		SCALE NONE		SHEET 9 OF 9	

C1045CPU

0880-EC-172840

REF. 1045CPU.SCH

SHEET 9