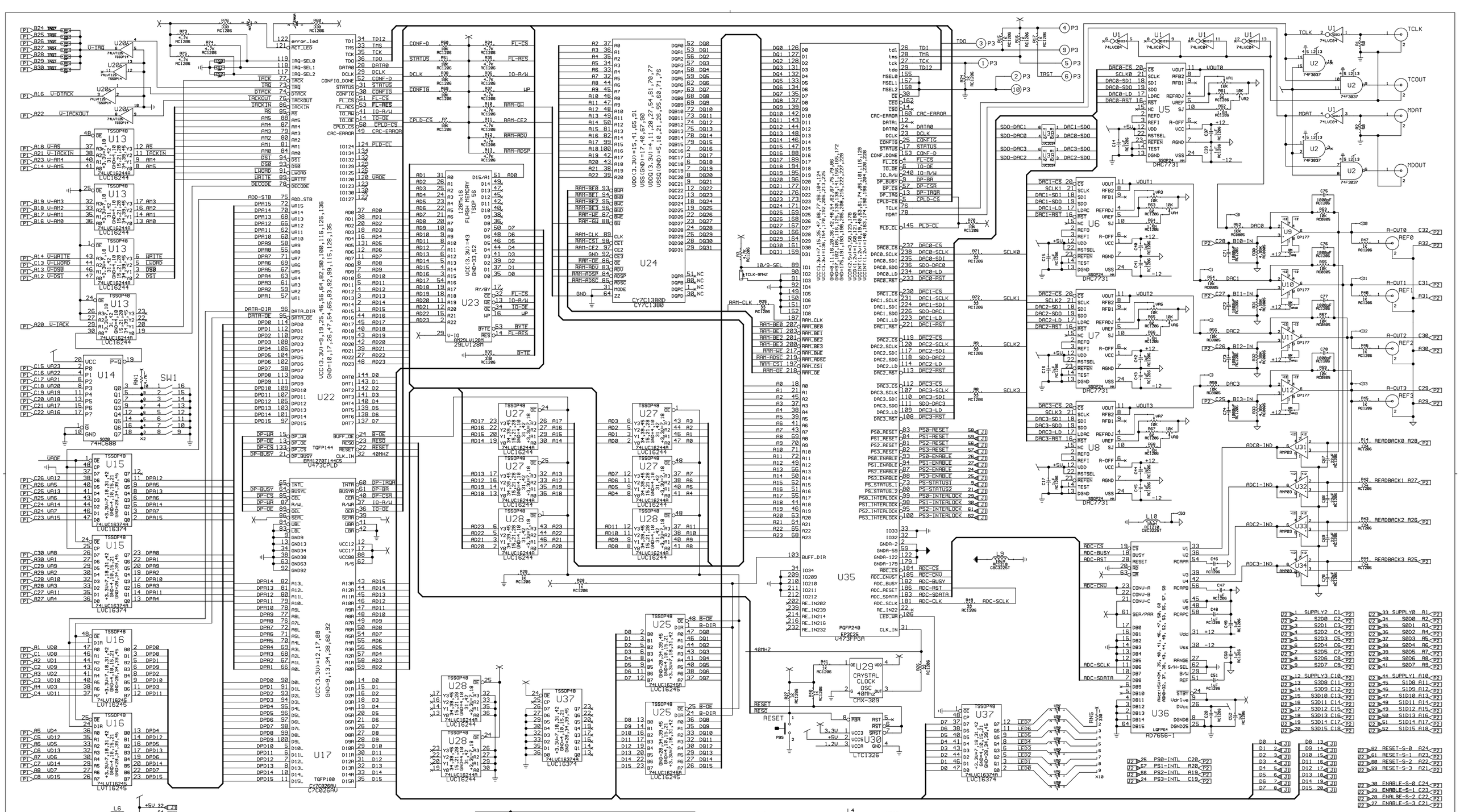
U13 74LVC16244
U14 74HC688
U15 74LVC16374
U16 74LVT16245
U22 EPM7128TI144S
V473CPLD
U23 29LV128H
FLASH MEMORY
128M (16M x 8)
TSOP 56
U24 CY7C1380D
CY7C1380
U35 PQFP240
EP3C25
V473FPGA
U36 AD7656-1
U17 CY7C026AV
C7C026AV
U18 TPS62050
U19 TPS62050
U21 TPS62050
U30 LTC1326
U29 CRYSTAL CLOCK OSC 40MHz
CMX-309
U5 DRC7731
U6 DRC7731
U7 DRC7731
U8 DRC7731
U2 74F3037
U9 OP177
U10 OP177
U11 OP177
U12 OP177
U31 RMP03
U32 RMP03
U33 RMP03
U34 RMP03
SW1
V-IRQ
V-DTACK
V-TACKOUT
V-AM3
V-AM2
V-AM1
V-AM0
V-WRITE
V-LWORD
V-DSR
V-DST
V-TACK
TCLK
TCOUT
MDAT
MDOUT
TDO
TDI
TMS
TCK
TRST
CONF-D
STATUS
DCLK
CONFIG
CPLD-CS
CRC-ERROR
FL-CS
FL-RES
IO-R/W
IO-OE
RAM-GW
RAM-CE2
RAM-ADV
RAM-ADSP
WP
BYTE
VAOE
PLD-CL
ADD-STB
DATA-DIR
DATA-OE
DP-WR
DP-OE
DP-CS
DP-BUSY
DP-IRQR
DP-BR
DP-CSR
B-OE
B-DIR
RESET
RESO
40MHZ
SCLK0
SCLK1
SCLK2
SCLK3
ADC-SCLK
ADC-SDATA
ADC-CS
ADC-BUSY
ADC-RST
ADC-CNV
ADC0-IND
ADC1-IND
ADC2-IND
ADC3-IND
READBACK0 A28
READBACK1 A27
READBACK2 A26
READBACK3 A25
A-OUT0
A-OUT1
A-OUT2
A-OUT3
PS0-INTL
PS1-INTL
PS2-INTL
PS3-INTL
RESET-S-0
RESET-S-1
RESET-S-2
RESET-S-3
ENABLE-S-0
ENABLE-S-1
ENABLE-S-2
ENABLE-S-3
+5VDC
+5V
3.3V
1.2V
VCCD
ORIGINATOR D. McArthur 4/22/09 CHECKED
DRAWN J. Gomilar 3/17/11 APPROVED
FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY
Accelerator Controls
VME V473
4 Channel Fast Ramp Card
SCALE
FILMED
DRAWING NUMBER
REV. B