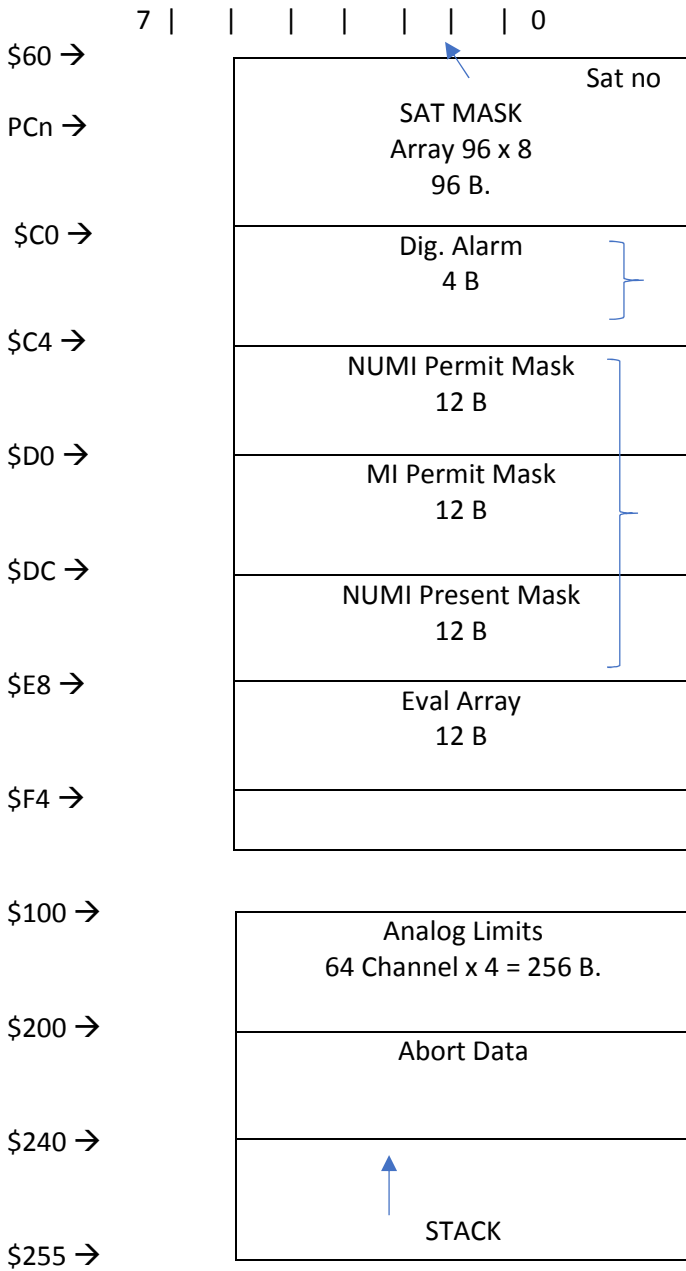


C204 Info

Page	NV Ram Main		
0	SAT (0) – (7) 0-7F 128 B		
1	Analog 00 - FF Limits 256 B	Lo	L H
		Hi	L H
2	00 – BF 192 B PCn's. Channel Configs		
3	00 – 07 - Time Delays 08 – 09 - Hi/Lo Beam Int ns. 0A - - No Act. PCn's		
4			
5			
6	NU		
7	NU		

SRAM – Remote



bits
= Good State

bit = 1
if not dependent on state

bit = 0
if dependent on state

Result of evaluation
of SAT MSK + other bits
= 1, test channel
= 0, skip

SRAM Main

(after download)

\$060 →

Sat Tables
Moved Here 128 B

\$0E0 →

Sat State Table
32 B

\$100 →

Storage for trip Values
64 Analog Ch.
128 B

\$180 →

Storage for read all values
(analog)
128 B

\$200 →

Read of Dig. Values
4 B

Up to 16 Trip PCn's
16 B

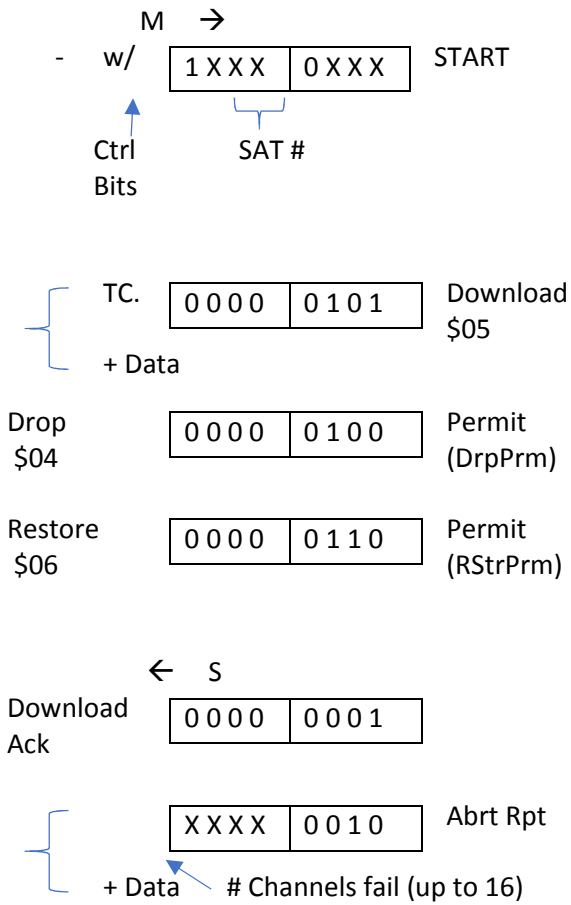
4 Timers
8 B

\$25F →

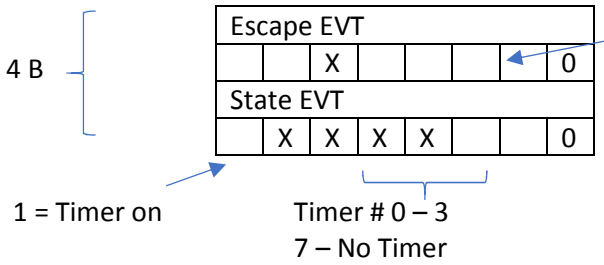


STACK

SPI Dialogue



Typical SAT State Table



bit 7 = 0 Escape Evt = TCLK
 = 1 Escape Evt = BS
 bit 6 = 0 State Evt = TCLK
 = 1 State Evt = BS
 bit 0-2 -- Current State
 bit 3 - Trigger bit
 bit 4 - = 1 Skip Tbl

\$88

J. Fitzgibbon - 2 Muti-Matrix Books

SRAM - Remote

SRM Mem. (after download)

- \$C00 - SAT Tables moved Here 128B
- \$D00 - S.t Start Table 32B
- \$E00 - Storage for trip values by analog ch. 128B
- \$F00 - Storage for real all values (analogs) 128B
- \$700 - Real of Dig. Values 4B
- \$701 - Trip Plan's 16B
- \$702 - 4 Timers 8B
- \$2F - STACK

NV RAM MAIN

AGF

- 0 - SAT(0-11) 0-7F 128B
- 1 - Analog 00-FF Lo Hi
- 2 - Limits 256B
- 3 - 00-BF 128B PCN's (channel analysis)
- 4 - 00-07 - Time Delays
- 5 - 08-0F - Hi/Lc Beam Inten
- 6 - 01 - No. Rd. PCN's
- 7 - NU
- 8 - NU

SPI Dialogue

M → S

1xxx 0xxx - W/START

0000 0101 - TC Download 405

1000010100 - Drop Permit

0000 0110 - Restore Permit 406 (Rst Per)

S → M

0000 0001 - Download ACK

xxxx 0010 - Abort Rpt

#channels + Data fall (up to 16)

Typical SAT State Table

Escape EVT							
		X					0
State EVT							
	X	X	X	X			0

↑ = Timer on

7 - No timer

bit 7 = 0 Esc. Evt = TCLK
 = 1 " " = BS
 bit 6 = 0 State Evt = TCLK
 = 1 " " = BS
 bit 0-2 - Current State
 bit 3 - Trigger bit
 bit 4 - = 1 skip Tbl

1011 - analog limits 64ch x 4 = 256 B

F1FF - Abort Dir

240 - STACK