



NOTES:
 The following circuits are only used for:
 BS CLK:
 - input stage 7.5 MHz with a comparator U4
 - ALTERA chip U13
 TCLK:
 - PMDC circuit (a daughter PCB)
 - IC chips U5, U17, and U19

NOTE:
 'x' = unused I/O pins.
 C0 = Bypass capacitor 0.1 uf,
 CP = Polar. capacitor 25 uf,
 L2-L6 = Inductor, ferrite beads

CONTRACT NO.		ENCODER OPTIC TRANSMITTER TEC	
APPROVALS	DATE	5/21	
DESIGN	V. RYTCHENKOV		
CHECKED			
ISSUED			
SIZE	FORM NO.	IND. NO.	REV.
D			
SCALE	SUBSET		