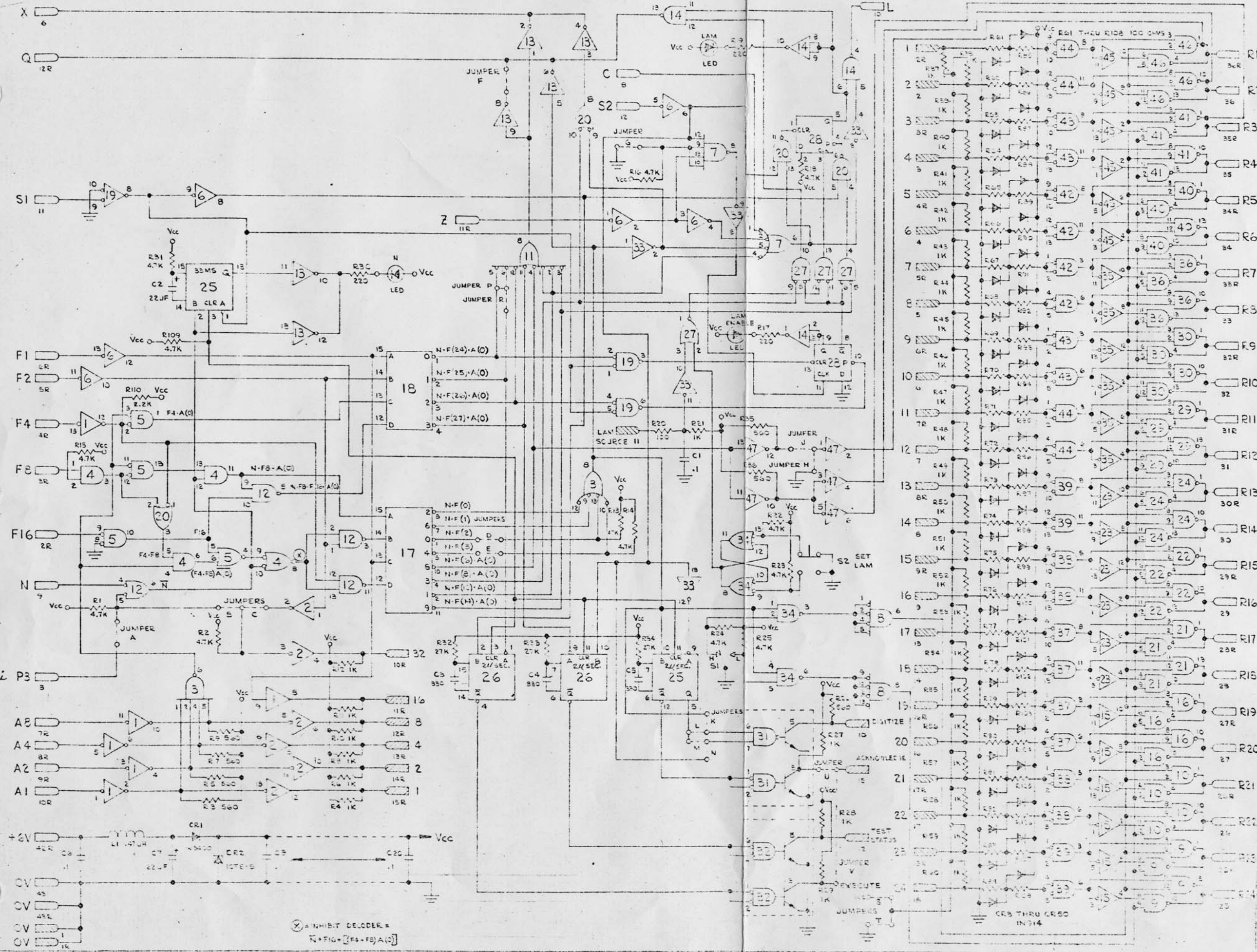


REVISIONS			
REV.	DESCRIPTION	DATE	INITIALS
A	SEE ECO#102	2-27-77	
B	SEE ECO#221	2-28-76	
C	SEE ECO#241	11-15-76	



INTEGRATED CIRCUITS				
REFERENCE DESIGNATIONS	PART NO.	Vcc	GND	
12, 20, 24	SN7400	14	7	
9, 10, 14, 16, 21, 22, 24, 25, 26, 30, 34, 40, 41, 45	SN7401	14	7	
5, 27	SN7402	14	7	
6, 15, 28, 35, 35, 45	SN7404	14	7	
2, 13, 47	SN7405	14	7	
4	SN7408	14	7	
1	SN7417	14	7	
3, 7	SN7422	14	7	
11	SN7430	14	7	
13	SN7432	14	7	
8	SN7445	14	7	
17, 18	SN7442	16	8	
25	SN7474	14	7	
25, 26	SN74122	16	8	
27, 29, 39, 42, 43, 44	SN74182	14	7	
31, 32	SN75451	B	4	

- NOTES:
1. ALL RESISTORS ARE 1/4 WATT, 10% AND VALUES ARE IN OHMS UNLESS OTHERWISE SPECIFIED.
 2. ALL CAPACITOR VALUES ARE IN PF UNLESS OTHERWISE SPECIFIED.
 3. ALL DIODES ARE 1N914 UNLESS OTHERWISE SPECIFIED.
 4. DATAWAY CONNECTOR INPUT-OUTPUT CONNECTOR

JORWAY CORP.
 SCALE: NONE
 DATE: 1-20-77
 MODEL: 207-24-BIT INPUT GATE
 207-0-001

ORIGINATOR	NATIONAL ACCELERATOR LABORATORY
DATE	1-20-77
DRAWN	R. ROSS
APPROVED	
SCALE	
SHEET	1
DRAWING NO.	0382-ED-36822
REV	

⊗ = INHIBIT DECODER =
 N-F(1) = (F4+FB)A(1)