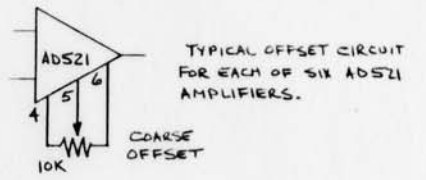
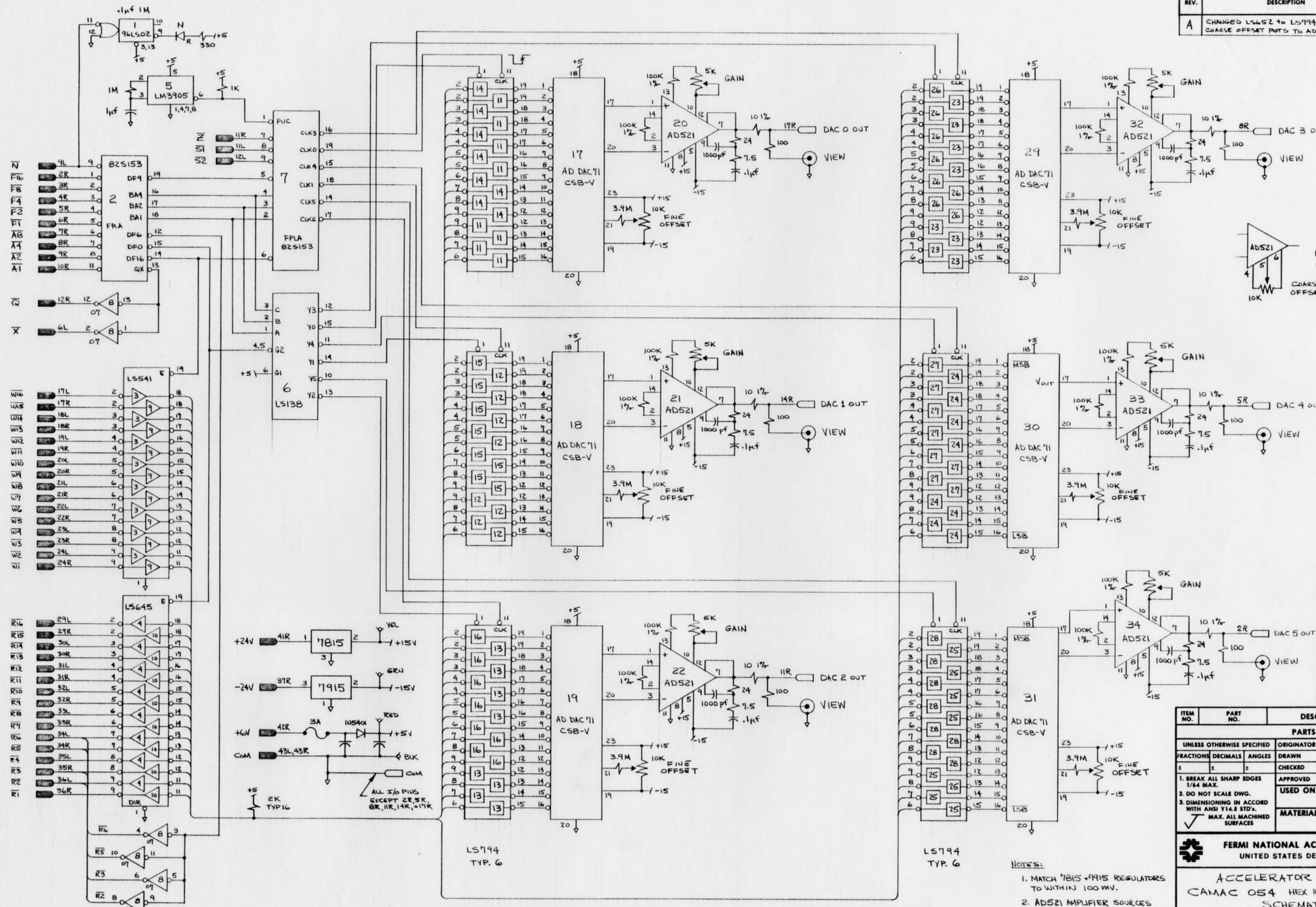


REV.	DESCRIPTION	DRAWN	DATE
A	CHANGED LS652 TO LS794, ADDED COARSE OFFSET POTS TO AD521	R. Ducar	6-13-84



PRINTED CIRCUIT BOARD	
TITLE	DWG NO
SCHEMATIC	ED 35811
ARTWORK	BD 35812
MASTER DWG	BD 35813
ASSEMBLY DWG	ED 35814
OUTLINE DWG	B
SOLDER MASK	B
SILKSCREEN	B
PARTS LIST	BP
FRONT PANEL	MC-35815

ITEM NO.	PART NO.	DESCRIPTION OR SIZE	QTY.	REQ.
PARTS LIST				
UNLESS OTHERWISE SPECIFIED		ORIGINATOR	R. DUCAR	2-21-84
FRACTIONS	DECIMALS	ANGLES	DRAWN	
1	2	3	CHECKED	
1. BREAK ALL SHARP EDGES 1/64 MAX.		APPROVED	R. Ducar	2-24-84
2. DO NOT SCALE DWG.		USED ON		
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD.		MATERIAL		
MAX. ALL MACHINED SURFACES				

FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY

ACCELERATOR CONTROLS
CAMAC 054 HEX 16BIT UNIPOLAR DAC
SCHEMATIC RF-107A

SCALE	FILMED	DRAWING NUMBER	REV.
		0812-ED-35811	A

- NOTES:**
1. MATCH 7815 +7915 REGULATORS TO WITHIN 100 MV.
 2. AD521 AMPLIFIER SOURCES 10 mA AT PWS FULL SCALE.