



- NOTES:**
- FOR BIPOLAR OUTPUT - USE AD DAC711 - COB-V AND WIRE JUMPER TO INVERT MSB TO DAC.
 - UNIPOLAR ALIGNMENT:
 - PROGRAM *0000. ADJ COARSE OFFSET FOR 0.0V OUT.
 - PROGRAM *FFFF. ADJ GAIN FOR +10.0V OUT.
 - PROGRAM *0000. ADJ FINE OFFSET FOR 0.0V OUT.
 - BIPOLAR ALIGNMENT:
 - PROGRAM *0000. ADJ COARSE OFFSET FOR -10.0V OUT.
 - PROGRAM *7FFF. ADJ GAIN FOR +10.0V OUT.
 - PROGRAM *0000. ADJ FINE OFFSET FOR 0.0V OUT.

PRINTED CIRCUIT BOARD	
TITLE	DWG NO.
SCHEMATIC	ED-218060
ARTWORK	BD-218061
MASTER DWG	BD-218062
ASSEMBLY DWG	
OUTLINE DWG	MD-34863
SOLDER MASK	
SILKSCREEN (FP)	MC-218064
PARTS LIST	
FRONT PANEL	MC-218063

ITEM NO.	PART NO.	DESCRIPTION OR SIZE	QTY. REQ.
PARTS LIST			
UNLESS OTHERWISE SPECIFIED		ORIGINATOR	R. DUCAR
FRACTIONS		DRAWN	8-14-85
DECIMALS		CHECKED	
ANGLES		APPROVED	R. Ducar
1. BREAK ALL SHARP EDGES 1/64 MAX.		USED ON	8-26-85
2. DO NOT SCALE DWG.		MATERIAL	
3. DIMENSIONING IN ACCORD WITH ANSI Y14.5 STD.			
FERMI NATIONAL ACCELERATOR LABORATORY UNITED STATES DEPARTMENT OF ENERGY			
ACCELERATOR CONTROLS CAMAC 055 - VECTORED 16 BIT DIGITAL OUTPUT/DAC SCHEMATIC			
SCALE	FILMED	DRAWING NUMBER	REV.
		0812-ED-218060	