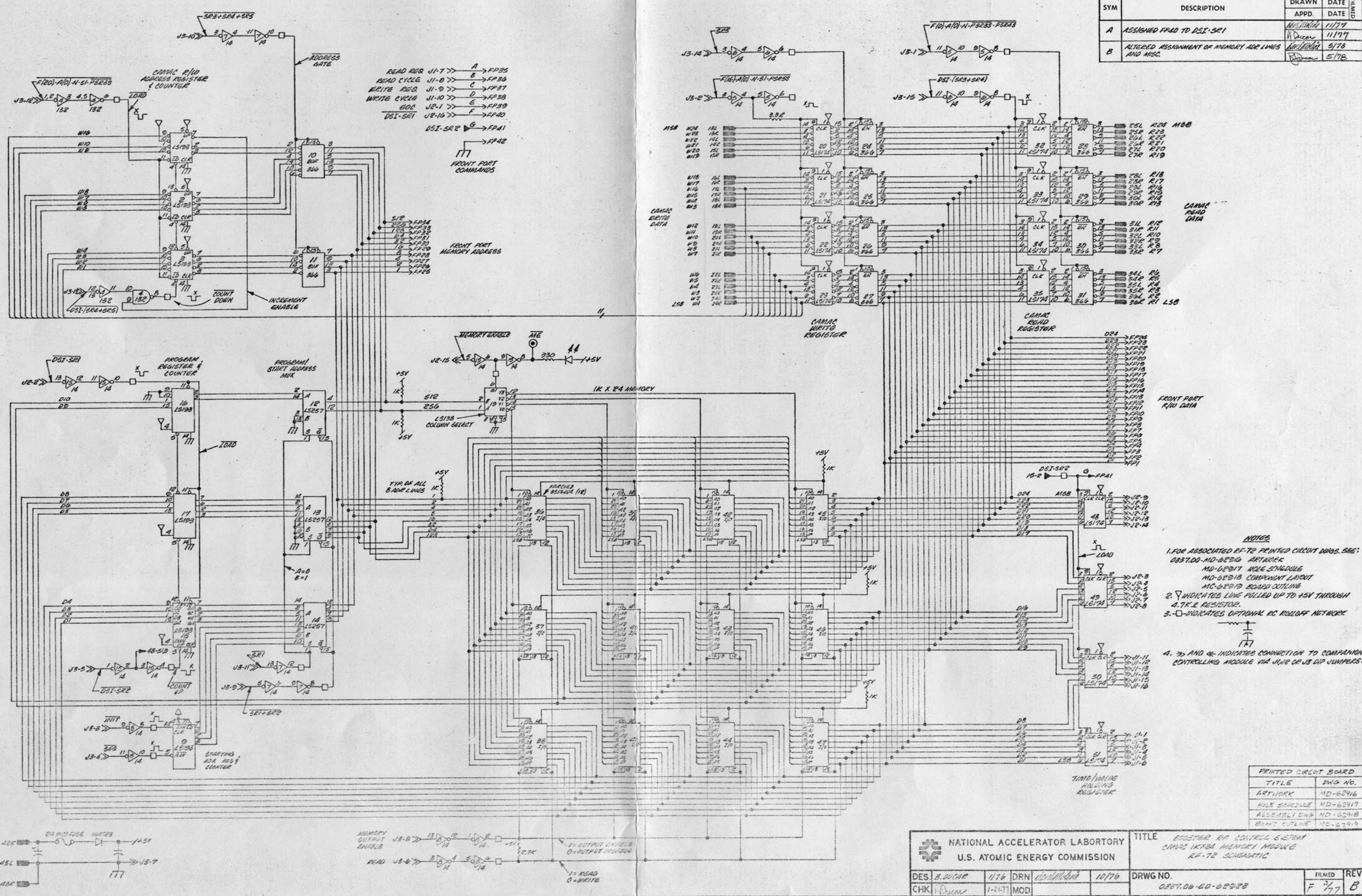


REVISIONS				
SYM	DESCRIPTION	APPD.	DATE	TIME
A	ASSIGNED FP40 TO DSI-SR1	M. D. DUGAN	11/77	
B	ALTERED ASSIGNMENT OF MEMORY ADDRESS AND MISC.	M. D. DUGAN	5/78	



- NOTES**
- FOR ASSOCIATED RF-72 PRINTED CIRCUIT BOARDS, SEE: 0327.00-MD-62916 NETWORK, MD-62917 REG. SINGLE, MD-62918 COMPONENT LAYOUT, MD-62919 BOARD OUTLINE.
 - INDICATED LINE PULLED UP TO +5V THROUGH 4.7K RESISTOR.
 - INDICATES OPTIONAL RC ROLLOFF NETWORK.
 - AND & INDICATES CONNECTION TO COMPANION CONTROLLING MODULE VIA J12 OF J3 DIP JUMPPERS.

PRINTED CIRCUIT BOARD	
TITLE	DWG NO.
ARTWORK	MD-62916
WIRE SCHEDULE	MD-62917
ASSEMBLY ENG	MD-62918
BOARD OUTLINE	MD-62919

		NATIONAL ACCELERATOR LABORATORY U.S. ATOMIC ENERGY COMMISSION		TITLE BOOSTER RF CONTROL SYSTEM CANAL 1K X 24 MEMORY MODULE RF-72 SCHEMATIC	
DES. B. DUGAN	1/76	DRN. [signature]	10/76	DRWG NO.	0327.06-ED-62923
CHK. [signature]	1-21-77	MOD.		FILMED	F 1/77
				REV.	B